DEPARTMENT OF MATHEMATICS AND COMPUTER SCIENCE
ODENSE UNIVERSITY

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A Practical Real-Time Systolic Integer Multiplier

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Abstract: We describe a construction of a practical real-time systolic integer multiplier. Our multiplier requires roughly half the hardware (number of half-adders) as compared to Atrubin's multiplier, which is the best real-time systolic multiplier known to-date. We demonstrate how the systematic use of design methodologies, including retiming, leads to efficient and easy to understand circuits.

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