Von Neumann architecture — (bottleneck — memory slower than processor)

◆□▶ ◆□▶ ★□▶ ★□▶ □ のQ@

Registers:

- general purpose
- special purpose
 - program counter
 - instruction register
 - others...

Adding 2 values from memory:

- 1. Get first value in a register
- 2. Get second value in a register
- 3. Add results in ALU result in a register

(ロ) (型) (E) (E) (E) (O)

4. Store result in memory (or a register)

Computer architecture

Example machine language — Appendix C Instruction:

- 4 bits op-code
- 12 bits operands
 - 4 bits register
 - ▶ 8 bits address 256 words in memory

How many general purpose registers are there?

- A. 4
- **B**. 8
- C. 12
- C. 12
- D. 16
- E. 32

Vote at m.socrative.com. Room number 415439.

(ロ) (型) (E) (E) (E) (O)

Example machine language

Instructions:

| Op-code | Operands | Meaning | | | |
|---------|-------------|--|--|--|--|
| 1 | RXY | Load reg R from memory cell XY | | | |
| 2 | RXY | Load reg R with value XY | | | |
| 3 | RXY | Store contents of reg R in cell XY | | | |
| 4 | 0 <i>RS</i> | Move contents of reg R to reg S | | | |
| 5 | RST | Add two's compl. contents of reg S to reg T; | | | |
| | | store result in R | | | |
| 6 | RST | Foating point add | | | |
| 7 | RST | OR | | | |
| 8 | RST | AND | | | |
| 9 | RST | XOR | | | |
| A | R0X | Rotate reg R X bits to right | | | |
| В | RXY | Jump to XY if $c(R) = c(0)$ | | | |
| С | 000 | HALT | | | |

Note operands are hexadecimal.

Example machine language

One word (cell) is 1 byte. One instruction is 16 bits.

Machine cycle:

fetch — get next instr., increment program counter by 2

・ロト ・ 日 ・ ・ 日 ・ ・ 日 ・ ・ つ へ ()

- decode
- execute (instr)

Example: check if low-order 4 bits of value in reg 1 = 0

| load zero into reg 0 |
|---|
| load string 00001111 into reg 2 |
| c(reg 1) AND c(reg 2) \longrightarrow reg 3 — masking |
| jump to address XY if $c(reg 3) = c(reg 0)$ |
| |

<□▶ <□▶ < □▶ < □▶ < □▶ < □ > ○ < ○

Example machine language

How can we complement a byte in reg 1?

- A. load 11 in register 2; OR 3,1,2;
- B. load FF in register 2; OR 3,1,2;
- C. load 00 in register 2; XOR 3,1,2;
- D. load 11 in register 2; XOR 3,1,2;
- E. load FF in register 2; XOR 3,1,2;

Vote at m.socrative.com. Room number 415439.

◆□▶ ◆□▶ ◆□▶ ◆□▶ ● ● ●

Computer architecture

- RISC reduced instr. set fast per instr. cell phones
- CISC complex instruction set easier to program PC

Clock

- coordinates activities
- faster clock \rightarrow faster machine cycle
- Hz one cycle per second
- MHz mega Hz (1 million Hz)
- ► GHz giga Hz (1000 MHz)
- flop floating point ops / sec
- benchmark program to run on different machines for comparison

External devices



motherboard — main circuit board (with CPU, memory)

controller — on motherboard or plugged into motherboard To reduce number — universal serial bus (USB), FireWire, Thunderbolt Serial — 1 bit at a time (vs. parallel) — fast for short distances

DMA — CPU not involved after starting — (read sector of disk)

うして ふゆう ふほう ふほう うらつ

If everything uses bus, von Neumann bottleneck.

External devices

Initial connection

- handshaking (also for protocols)
- often status word is printer OK, paper out, jam,...

Communication rates

- bits per second (bps) / bytes per second (Bps)
- Kbps standard phone lines
- Mbps 1,000,000 bps USB, FireWire 100s of Mbps
- ▶ Gbps 1,000,000,000 bps USB 3.0, Thunderbolt Gbps

External devices

(Time-division) multiplexing

| telephone dat | data from | telephone | |
|-------------------|-----------|-----------|--|
| voice | computer | voice | |

▲□▶ ▲圖▶ ▲臣▶ ★臣▶ ―臣 …の�?

data from computer can be modem, xDSL, cable TV

bandwidth – max rate broadband – high rate

Making computers faster

▶ Pipelining — ADD RXY
 ▲ ADD R'X'Y'
 ▲ ADD R'X'Y'
 ▲ ADD R'X'Y''

fetch instruction decode perform add possibly further divided

Supercomputers

 multiprocessor machines now
 (10s of 1000s, with over 3,000,000 cores)
 SIMD, MIMD

Multi-core — in single integrated circuit, package

- dual-core 2 processors
- quad-core 4 processors
- ► ...
- 2 at 2 GHz not as good as 1 at 4 GHz

Operating systems



Utilities — unclear boundaries with other things anti-virus program, formatting a disk, operations with resources, cryptography

browser — no (Internet Explorer?)

Operating systems

User interface = shell

- Command window
- GUI graphical user interface icons, clicking, windows manager



Windows

◆□▶ ◆□▶ ★□▶ ★□▶ □ のQ@

Basic functions

Basic functions in kernel

- 1. File manager
 - directories (folders) organization
 - ▶ path ~joan/WWWpublic/intro/15slide4.pdf

うして ふゆう ふほう ふほう うらつ

allows access, checks rights

2. Device drivers

- printer, screen, mouse, etc.
- communicate with controllers

Basic functions

3. Memory manager

- in multiuser or multitask system, much to do
- virtual memory if more data than for physical memory
- store some pages in physical memory
 - if used often, leave there paging is slow
- 4. Scheduler and dispatcher
 - giving time slices to different tasks or users
- 5. Bootstrap
 - bootstrap program (boot loader) in ROM (non-volatile)
 - loads rest of OS from disk into main memory (volatile)

うして ふゆう ふほう ふほう うらつ